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Q2  
a first transistor device having a first terminal connected to the first switch node, a second terminal, and a third terminal configured to receive a control signal that controls the electrical connectivity between the first terminal and the second terminal;

a second transistor device having a first terminal connected to the second terminal of the first transistor device, a second terminal connected to the second switch node, and a third terminal configured to receive the control signal; and

a third transistor device having a first terminal connected to the first terminal of the first transistor device, a second terminal connected to the second terminal of the second transistor device, and a third terminal configured to receive the control signal.

### **In the Drawings**

Please amend the Drawings by making the modifications as indicated in red permanent ink on the separate accompanying sheet(s). Applicants note that the amendments to the Drawings were made at the behest of the Examiner and that no new matter has been added.

### **REMARKS**

This is a full and timely response to the outstanding non-final Office Action mailed November 11, 2001. Reconsideration and allowance of the application and presently pending claims are respectfully requested.

#### **I. Present Status of Patent Application**

Upon entry of this response claims 1 – 8 are pending in the present application. Claims 1 and 6 have been amended as set forth above. Support for these amendments is found in the specification and in the original claims. No new matter has been added.